

## **AMENDMENTS TO SPECIFICATION**

In the specification, please amend the following ¶ [0021]:

[0021] Memory device 390 provides a number of memory cells within storage array 399 to store data that may be provided and/or required by controlling device 320. Interface buffer 392 within memory device 290 receives read and write commands across memory bus 380a, and accesses appropriate memory cells within storage array 399 to either store or retrieve data as commanded. In support of carrying out refresh operations on memory cells within storage array 399, interface buffer 392 incorporates logic to initiate refresh operations, such as refresh timer 394 and/or refreshing tracking logic [[295]] 395.